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PATENT NUMBER and
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U.S. UTILITY Patent Application

10/009826

APPL NUM 10009826	FILING DATE 03/19/2002	CLASS 377	SUBCLASS 257	GAU 2016	EXAMINER R. NGUYEN
**APPLICANTS: Matsuoka Hideyuki; Sakata Takeshi; Inura Shinichiro; Yamanaka Toshiaki; Kachi Tsuyoshi; Sekiguchi Tomonori;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/03723 03/08/2000					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 11/171557 06/17/1999					
BEST AVAILABLE COPY					
PG-FUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		XA-9594			
Verified and Acknowledged Examiners's initials					
TITLE : Semiconductor memory device and method of manufacturing the same					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for 0.0
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn / Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL		PREPARED FOR ISSUE	
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